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ÿThis book is exclusively designed for the first-year engineering students of Jawaharlal Nehru Technological University, Kakinada studying the ?Network Analysis? course in their second semester. The primary goal of this text is to enable the student have a firm grasp over basic principles of Network Analysis, and develop an understanding of circuits and the ability to design practical circuits that perform the desired operations. Emphasis is placed on basic laws, theorems and techniques which are used to develop a working knowledge of the methods of analysis used most frequently in further topics of electrical engineering. Each chapter begins with principles and theorems together with illustrative and other descriptive material. A large number of solved examples showing students the step-by-step processes for applying the techniques are presented in the text. Several questions in worked examples have been selected from university question papers. As an aid to both the instructor and the student, objective questions and tutorial problems provided at the end of each chapter progress from simple to complex. Answers to selected problems are given to instil confidence in the reader. Due care is taken to see that the reader can easily start learning the concepts of Network Analysis without prior knowledge of mathematics. Salient Features ? 100% coverage of JNTU Kakinada latest syllabus ? Individual topics

very well supported by solved examples ? Roadmap to the syllabus provided for systematic reading of the text ? University questions incorporated at appropriate places in the text ? Excellent pedagogy: ? Solved Examples: 490 ? Practice Problems: 214 ? Objective Type Questions: 191 ? Illustrations: 915

The revision of this extremely popular text, *Circuits and Networks: Analysis and Synthesis*, comes at a time when the industry is increasingly looking to hire engineers who are able to display learning outcomes. The book has been revised based on internationally accepted Learning Outcomes required from a course. Additionally, key pedagogical aids, such as questions from previous year question papers are added afresh to further help students in preparing for this course and its examinations. For the tech savvy, the practice of MCQs in a digital and randomized environment will provide thrill. Salient Features: - Content revised as per internationally accepted learning outcomes - 461 Frequently asked questions derived from important previous year question papers - Features like Definition and Important Formulas are highlighted within the text

This book caters to a course on *Circuits and Networks* with coverage of both Analysis and Synthesis. Lucid language, fundamental discussions and illustrative examples are some of the excellent features of this text. There are numerous solved examples employing the step wise problem solving approach which helps in easy grasping of the concepts by the students. The numericals employ both AC and DC methods of analysis. Multiple Choice Questions and Practice problems have been provided in plenty and are of graded challenge levels, helping the students to prepare for competitive examinations. PSpice problems have been incorporated to help in simulation.

Pulse and Digital Circuits is designed to cater to the needs of undergraduate students of electronics and communication engineering. Written in a lucid, student-friendly style, it covers key topics in the area of pulse and digital circuits. This is an introductory text that discusses the basic concepts involved in the design, operation and analysis of waveshaping circuits. The book includes a preliminary chapter that reviews the concepts needed to understand the subject matter. Each concept in the book is accompanied by self-explanatory circuit diagrams. Interspersed with numerous solved problems, the text presents detailed analysis of key concepts. Multivibrators and sweep generators are covered in great detail in the book.

Foreword -- Foreword to the First Printing -- Preface -- Chapter 1 -- Introduction -- Chapter 2 -- Message Switching Layer -- Chapter 3 -- Deadlock, Livelock, and Starvation -- Chapter 4 -- Routing Algorithms -- Chapter 5 -- CollectiveCommunicationSupport -- Chapter 6 -- Fault-Tolerant Routing -- Chapter 7 -- Network Architectures -- Chapter 8 -- Messaging Layer Software -- Chapter 9 -- Performance Evaluation -- Appendix A -- Formal Definitions for Deadlock Avoidance -- Appendix B -- Acronyms -- References -- Index.

This book has been designed as per the syllabus of Network Theory offered to the second year, second semester students of EEE in JNTU Hyderabad. A student centric approach has been adopted to enable easy understanding of the topics. Salient Feature: • Completely in sync with the syllabus of JNTU-Anantapur • Rich Pedagogy: o 197 Solved Examples and Problems o 104 Objective Type Questions o 109 Practice Problems

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This book on Network Analysis has been designed keeping in mind the students who take up this foundation course in their first semester at JNTU. Focused coverage of syllabus, variety of solved problems from previous years question papers and right level of theory makes this book very student friendly.

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